

Nov. 16		10F Song Bo Room	10F Chang Chin Room	V Fl. International Reception Hall		VIP Fl.	VIP Fl.	V Fl. V108	V Fl. V109
08:30-09:00 (30)	Registration								
09:00-10:20 (80)		Tutorial 1 Silicon Microelectronic Chips in the Human Healthcare and Life Sciences Lecturer: Donhee Ham (Harvard U.) Chair: Akira Matsuzawa (Tokyo Tech.)	Tutorial 2 Inductive-Coupling Through-Chip Interface for 3D System Integration Lecturer: Tadahiro Kuroda (Keio U.) Chair: Makoto Ikeda (U. of Tokyo)			Student Design Contest	Registration	Secretariat Room	Internet Lounge
10:20-10:40 (20)		Break							
10:40-12:00 (80)		Tutorial 1 Silicon Microelectronic Chips in the Human Healthcare and Life Sciences Lecturer: Donhee Ham (Harvard U.) Chair: Akira Matsuzawa (Tokyo Tech.)	Tutorial 2 Inductive-Coupling Through-Chip Interface for 3D System Integration Lecturer: Tadahiro Kuroda (Keio U.) Chair: Makoto Ikeda (U. of Tokyo)						
12:00-13:00 (60)		Lunch							
13:00-14:20 (80)		Tutorial 3 Emerging ADC design Lecturer: Un-Ku Moon (Oregon State U.) Chair: Akira Matsuzawa (Tokyo Tech.)	Tutorial 4 Advanced RF and Analog Design in the Nano-Meter Era Lecturer: Bram Nauta (Twente U.) Chair: Makoto Ikeda (U. of Tokyo)						
14:20-14:40 (20)		Break							
14:40-16:00 (80)		Tutorial 3 Emerging ADC design Lecturer: Un-Ku Moon (Oregon State U.) Chair: Akira Matsuzawa (Tokyo Tech.)	Tutorial 4 Advanced RF and Analog Design in the Nano-Meter Era Lecturer: Bram Nauta (Twente U.) Chair: Makoto Ikeda (U. of Tokyo)						
16:00-17:30 (90)									
17:30-18:00 (30)									
18:00- 19:30 (90)									
				Welcome Reception					

Nov. 17	10F Auditorium	10F Song Bo Room	10F Chang Chin Room	V Fl. V105	V Fl. V110	VIP Fl.	VIP Fl.	V Fl. V108	V Fl. V109	
08:00-08:30 (30)	Registration									
08:30-08:40 (10)	Opening Ceremony						Registration			
08:40-08:50 (10)	Welcome Remark by A-SSCC 2009 General Chair Ming-Kai Tsai (MediaTek Inc.)									
08:50-09:35 (45)	Plenary Talk 1 "Green Future: IC Packaging Opportunities Abound" Invited Speaker: Ho-Ming Tong (ASE Group) Chairs: Tohru Furuyama(Toshiba) Mototsugu Hamada (Toshiba)									
09:40-10:25 (45)	Plenary Talk 2 "Nanoelectronics Devices: More CMOS, Fusion CMOS and Beyond CMOS" Invited Speaker: Hisatsune Watanabe (SELET) Chairs: Tohru Furuyama(Toshiba) Mototsugu Hamada (Toshiba)									
10:25-10:55 (30)										SDC
10:55-12:35 (100)		Industry Program 1: Future Computing Technologies Chairs: Stefan Rusu (Intel) Shaojun Wei (Tsinghua U.)	Industry Program 2: Advances in Wireless and Multimedia Chairs: Changhyun Kim (Samsung) Koji Kai (Panasonic)							
12:35-13:35 (60)	Lunch									
13:40-15:45 (125)		Session 1: Analog Filters Chairs: Yong Ping Xu (NUS) Makoto Nagata (Kobe U.)	Session 2: RF Synthesizers Chairs: Howard Luong (HKUST) Toru Masuda (Hitachi)	Session 3: Low Power Circuit Techniques Chairs: Chi-Cheng Ju (MediaTek) Makoto Ikeda (U. of Tokyo)	Session 4: Bio and Emerging Applications Chairs: Ali Keshavarzi (TSMC) Koji Kotani (Tohoku U.)					
15:45-16:00 (15)	Break									
16:00-17:40 (100)		Panel Discussion 1 Toward Low Voltage (0.5V) Design: Will Challenges Limit Operation? Organizer: Ali Keshavarzi (TSMC)	Panel Discussion 2 Gigabit Transceiver: a Panacea or a Poison? Organizers: Jri Lee (NTU) Tadahiro Kuroda (Keio U.)							SDC
17:40-18:10 (30)	Break									
18:10-18:55 (45)		Special Lecture "All you need to know to write a winning ISSCC paper and give a smashingly good presentation" Invited Speaker: Jan Van Der Spiegel (U. of Pennsylvania) Chairs: Tohru Furuyama(Toshiba) Mototsugu Hamada (Toshiba)								
18:55-21:00 (125)	Conference Banquet (12F The Grand Ballroom)									

Nov. 18	10F Auditorium	10F Song Bo Room	10F Chang Chin Room	V Fl. V105	V Fl. V110		VIP Fl.	V Fl. V108	V Fl. V109	
08:00-08:30 (30)	Registration							Registration	Secretariat Room	Internet Lounge
08:30-09:15 (45)	Plenary Talk 3 "Weak Inversion for Ultra Low-Power and Very Low-Voltage Circuits" Invited Speaker: Eric Vittoz (EPFL) Chairs: Tohru Furuyama(Toshiba) Mototsugu Hamada (Toshiba)									
09:15-09:30 (15)	Break									
09:30-11:50 (140)		Session 5: Low Power Analog-to-Digital Converts Chairs: Seong Hwan Cho(KAIST) Leibo Liu (Tsinghua U.)	Session 6: Wireline Communication Circuits Chairs: Wei-Zen Chen (NCTU) Jung-Hoon Chun (SKKU)	Session 7: Image Signal Processing Chairs: Hirofumi Sumi (Sony) Shao-Yi Chien (NTU)	Session 8: Memory Chairs: Katsuyuki Sato (TSMC) Bor-Doou Rong (Etron)					
11:50-12:50 (60)	Lunch									
12:50-14:55 (125)		Session 9: Advanced Analog Techniques Chairs: Bill Yang Liu (Analog Devices) Tsung-Hsien Lin (NTU)	Session 10: RF Building Blocks and Phased Array Receivers Chairs: Shouhei Kousai (Toshiba) Ryangsu Kim (Panasonic)	Session 11: Low-power System Integration Techniques and Bio-medical DSPs Chairs: Fumio Arakawa (Renesas) Tai-Cheng Lee (NTU)	Session 12: Inductive Link and Clock Generation Chairs: Masaki Hirata (NEC) Jri Lee (NTU)					
14:55-15:15 (20)	Break									
15:15-17:20 (125)		Session 13: Power Management Chairs: Yasuhiro Sugimoto (Chuo U.) Po-Chiun Huang (NTHU)	Session 14: RF Transceivers and SoC Chairs: Hyunchol Shin (Kwangwoon U.) Chien-Nan Kuo (NCTU)	Session 15: Digital Communication Chairs: Tomohisa Wada (U. of the Ryukyus) Zhiyi Yu (Fudan U.)						